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Notice of Allowability	10/757 115	
	10/757,115 Examiner	SMITH ET AL.
	Liez Neuron	0040
	Hiep Nguyen	2816
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED ir or other appropriate commu IGHTS. This application is s	n this application. If not included unication will be mailed in due course. THIS
1. X This communication is responsive to <u>amendment filed on 06-07-07</u> .		
2. \square The allowed claim(s) is/are <u>2-16 and 21-25</u> .		
 3. □ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) □ All b) □ Some* c) □ None of the: 		
a) All b) Some* c) None of the: 1. Certified copies of the priority documents have been received.		
2. Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE .		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
 hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's 		in the Office action of
Paper No./Mail Date	•	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s) 1. X Notice of References Cited (PTO-892)	5 🗖 Notice of In	formal Patent Application
 Notice of Draftperson's Patent Drawing Review (PTO-948) 		ummary (PTO-413),
3. Information Disclosure Statements (PTO/SB/08),	Paper No./	/Mail Date Amendment/Comment
Paper No./Mail Date 4.	8. 🕅 Examiner's	Statement of Reasons for Allowance
of Biological Material	. —	
	9. 🗋 Other	

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Application/Control Number: 10/757,115 Art Unit: 2816

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Reason for allowance

The following is an examiner'statement of reasons for allowance: the prior art of records (2007/0080723) fails to show a buffer circuit comprising: a first transistor having a gate, a source, and a drain, wherein the source of the first transistor is connected to the input terminal; a second transistor having a gate, a source, and a drain, wherein the gate of the first transistor is connected to the drain and the gate of the second transistor, wherein the source of. the second transistor is connected to the output terminal; a third transistor having a gate, a source, and a drain, wherein the gate of the third transistor is connected to the drain of the first transistor, wherein the source of the third transistor is connected to the drain of the first transistor, wherein the source of the third transistor is connected to the drain of the first transistor, wherein the source of the third transistor is connected to the drain of the first transistor, wherein the source of the third transistor is connected to the drain of the first transistor, wherein the source of the third transistor is connected to the drain of the first transistor; and a second transistor; a first current source connected to the drain of the first transistor; and a second current source connected to the drain of the first transistor; and a second current source connected to the drain of the first transistor; and a second current source connected to the drain of the second transistor as called for in claims 2, 7, 9, 10 and 21. Therefore, claims 2-16 and 21-25 are allowed.

Conclusion

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hiep Nguyen whose telephone number is (571) 272-1752. The examiner can normally be reached on Monday to Friday from 7:30am to 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Hiep Nguyen

06-14-07

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